



SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR
(Autonomous)

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)
(Accredited by NBA & Accredited by NAAC with 'A' Grade)
(An ISO 9001:2008 Certified Institution)
Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

Examinations Branch

TIME TABLE FOR EXTERNAL EXAMINATION

M.Tech I Year II Semester (R20) Regular Examinations November - 2021

EXTERNAL EXAM: 10:00 AM to 01:00 PM

Date/Day	STRUCTURAL ENGINEERING	VLSI	THERMAL ENGINEERING
18-11-2021 (THURSDAY)	20CE1005 FEM in Structural Engineering	20EC4209 FPGA Architectures & Applications	20ME3105 Advanced Heat Transfer
20-11-2021 (SATURDAY)	20CE1006 Structural Dynamics	20EC4210 Low Power VLSI Design	20ME3106 Steam Engineering
22-11-2021 (MONDAY)	20CE1013 Advanced Steel Design	20EC4212 Algorithms for VLSI Design Automation	20ME3118 Refrigeration and Cryogenics
24-11-2021 (WEDNESDAY)	20CE1017 Design of Advanced Concrete Structures	20EC4104 Real Time Operating System	20ME3122 Modeling of IC Engines

- Note:** 1. If any omissions or clashes in this time table may please be informed to the undersigned immediately.
2. Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual.


CONTROLLER OF EXAMINATIONS

Controller of Examinations
Siddharth Institute of Engineering & Technology,
(AUTONOMOUS)
PUTTUR - 517 583, Chittoor (DIST.) A.P


PRINCIPAL

PRINCIPAL
Siddharth Institute of Engineering & Technology
Siddharth Nagar,
PUTTUR - 517583, Chittoor (Dt.)